

# **Expressive modular verification of termination for busy-waiting programs**

**Work in progress**

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1. Verifying deadlock-freedom
2. Verifying absence of infinite recursion
3. Verifying termination of busy-waiting programs
4. Modular specifications
  - A. Logically atomic triples
  - B. Total correctness logically atomic triples with liveness assumption
5. Conclusion

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# Deadlock-freedom

```
let s = CreateSignal in  
  Await s || SetSignal s
```

# Deadlock-freedom

No one to wait on

```
let s = CreateSignal in
```

```
  Await s || 
```

# Deadlock-freedom

## Signals and obligations

let  $s = \text{CreateSignal}$  in  
 $\{\text{obs}(\{s\}) * \text{sig}(s, \text{false})\}$

$\exists b. \text{sig}(s, b)$

$\{\text{obs}(\emptyset)\}$		$\{\text{obs}(\{s\})\}$
Await $s$		SetSignal $s$
$\{\text{obs}(\emptyset)\}$		$\{\text{obs}(\emptyset)\}$

# Deadlock-freedom

## Circular dependencies

```
let s = CreateSignal in  
let t = CreateSignal in
```

```
Await s      || Await t  
SetSignal t  || SetSignal s
```

# Deadlock-freedom

## Circular dependencies

```
let s = CreateSignal in  
let t = CreateSignal in
```

```
Await s      || Await t  
SetSignal t  || SetSignal s
```





# Deadlock-freedom

```
let s = CreateSignal () in  
{obs({s}) * sig(s, false)}
```

```
∃b. sig(s, b)
```

{obs(∅)}		{obs({s})}
Await s		SetSignal s
{obs(∅)}		{obs(∅)}

# Deadlock-freedom

## Levels

let  $s = \text{CreateSignal } l \text{ in}$   
 $\{\text{obs}(\{s\}) * \text{sig}(s, l, \text{false})\}$

$\exists b. \text{sig}(s, l, b)$

$\{\text{obs}(\emptyset)\}$		$\{\text{obs}(\{s\})\}$
Await $s$		SetSignal $s$
$\{\text{obs}(\emptyset)\}$		$\{\text{obs}(\emptyset)\}$

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# Absence of infinite recursion

```
(μ loop n.  
  if n = 0 then ()  
  else  
    loop (n-1)) 10
```

# Absence of infinite recursion

## Call permissions

```
(μ loop n.  
  if n = 0 then ()  
  else
```

```
    burn δ in
```

```
    loop (n-1)) 10
```

# Absence of infinite recursion

## Call permissions

```
{n · cp(δ)}  
(μ loop n.  
  if n = 0 then ()  
  else  
    {n · cp(δ)}  
    burn δ in  
    {(n - 1) · cp(δ)}  
    loop (n-1) 10
```

# Absence of infinite recursion

## Call permissions

```
(μ loop n.  
  if n = 0 then ()  
  else  
    burn  $\delta_n$  receive  $\delta_{n-1}$  in  
    loop (n-1)) 10
```

# Absence of infinite recursion

## Call permissions

```
{cp( $\delta_n$ )}  
( $\mu$  loop n.  
  if n = 0 then ()  
  else  
    {cp( $\delta_n$ )}  
    burn  $\delta_n$  receive  $\delta_{n-1}$  in  
    {cp( $\delta_{n-1}$ )}  
    loop (n-1)) 10
```



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$\{obs(\emptyset)\}$   
**let**  $s = \text{CreateSignal } l$  **in**  
 $\{obs(\{s\}) * sig(s, l, false)\}$   
 $\exists b. sig(s, l, b)$

$\{obs(\emptyset)\}$	$\{obs(\{s\})\}$
<b>Await</b> $s$	<b>SetSignal</b> $s$
$\{obs(\emptyset)\}$	$\{obs(\emptyset)\}$

# Busy-waiting

$\{obs(\emptyset)\}$

let  $s = \text{CreateSignal } l$  in  
 $\{obs(\{s\}) * sig(s, l, false)\}$

$\exists b. sig(s, l, b)$

$\{obs(\emptyset)\}$

```
(μ loop ().  
  if !s then ()  
  else  
    loop ()) ()
```

$\{obs(\emptyset)\}$

$\{obs(\{s\})\}$   
SetSignal s  
 $\{obs(\emptyset)\}$

# Busy-waiting

$\{obs(\emptyset)\}$

let  $s = \text{CreateSignal } l$  in  
 $\{obs(\{s\}) * sig(s, l, false)\}$

$\exists b. sig(s, l, b)$

$\{obs(\emptyset)\}$

$(\mu \text{ loop } ().$   
  if !s then  $()$   
  else  
   ~~$\text{loop } ()$~~   $()$ )  $()$

$\{obs(\emptyset)\}$

$\{obs(\{s\})\}$   
 $\text{SetSignal } s$   
 $\{obs(\emptyset)\}$

# Busy-waiting

$\{obs(\emptyset)\}$

let  $s = \text{CreateSignal } l$  in  
 $\{obs(\{s\}) * sig(s, l, false)\}$

$\exists b. sig(s, l, b)$

$\{obs(\emptyset)\}$

```
( $\mu$  loop ().  
  if !s then ()  
  else
```

```
     $\{obs(\emptyset) * cp(\delta_0)\}$ 
```

```
    burn  $\delta_0$  in loop ()) ()
```

$\{obs(\emptyset)\}$

$\{obs(\{s\})\}$   
SetSignal  $s$   
 $\{obs(\emptyset)\}$

# Busy-waiting

$\{obs(\emptyset)\}$

let  $s = \text{CreateSignal } l$  in  
 $\{obs(\{s\}) * sig(s, l, false)\}$

$\exists b. sig(s, l, b)$

$\{obs(\emptyset)\}$

$(\mu \text{ loop } ()$   
  if !s then ()  
  else

~~$\{obs(\emptyset) * cp(\delta_0)\}$~~

  burn  $\delta_0$  in loop ()) ()

$\{obs(\emptyset)\}$

$\{obs(\{s\})\}$   
SetSignal s  
 $\{obs(\emptyset)\}$

```
{obs(∅)}  
let s = CreateSignal l in
```

```
{obs({s}) * sig(s, l, false)}
```

```
∃b. sig(s, l, b)
```

```
{obs(∅)}  
(μ loop () .  
  if !s  
    then ()  
  else  
    {obs(∅) * cp(δ0)}  
    burn δ0 in loop ()) ()  
{obs(∅)}
```

```
{obs({s})}  
SetSignal s  
{obs(∅)}
```

```

{cp( $\delta_1$ ) * obs( $\emptyset$ )}
let s = CreateSignal l in
CreateWaitPerm s  $\delta_1$   $\delta_0$ ;
{obs({s}) * sig(s, l, false) * waitp(s,  $\delta_0$ )}


$\exists b. \text{sig}(s, l, b)$


```

```

{obs( $\emptyset$ )}
( $\mu$  loop () .
  if !s
    then ()
  else
    {obs( $\emptyset$ ) * cp( $\delta_0$ )}
    burn  $\delta_0$  in loop ()) ()
{obs( $\emptyset$ )}

```

```

{obs({s})}
SetSignal s
{obs( $\emptyset$ )}

```



```

{cp( $\delta_1$ ) * obs( $\emptyset$ )}
let s = CreateSignal l in
CreateWaitPerm s  $\delta_1$   $\delta_0$ ;
{obs({s}) * sig(s, l, false) * waitp(s,  $\delta_0$ )}


$\exists b. \text{sig}(s, l, b)$


```

<pre> {obs(<math>\emptyset</math>) * waitp(s, <math>\delta_0</math>)} (<math>\mu</math> loop () .   if &lt;if !s then () else wait s <math>\delta_0</math>;     !s&gt; then ()   else     {obs(<math>\emptyset</math>) * waitp(s, <math>\delta_0</math>) * cp(<math>\delta_0</math>)}     burn <math>\delta_0</math> in loop ()) () {obs(<math>\emptyset</math>)} </pre>	<pre> {obs({s})} SetSignal s {obs(<math>\emptyset</math>)} </pre>
--	---

```

{cp( $\delta_1$ ) * obs( $\emptyset$ )}
let f = ref 41 in
let s = CreateSignal l in
CreateWaitPerm s  $\delta_1$   $\delta_0$ ;
{obs({s}) * sig(s, l, false) * waitp(s,  $\delta_0$ ) * f  $\mapsto$  41}
 $\exists b, n. \text{sig}(s, l, b) * f \mapsto n * n \neq 42 \rightarrow b = \text{false}$ 

```

<pre> {obs(<math>\emptyset</math>) * waitp(s, <math>\delta_0</math>)} (<math>\mu</math> loop () .   if &lt;if !f = 42 then () else wait s <math>\delta_0</math>;     !f = 42&gt; then ()   else     {obs(<math>\emptyset</math>) * waitp(s, <math>\delta_0</math>) * cp(<math>\delta_0</math>)}     burn <math>\delta_0</math> in loop () ) () {obs(<math>\emptyset</math>)} </pre>	<div style="border-left: 3px double black; height: 100%;"></div>	<pre> {obs({s})} &lt;f := 42; SetSignal s&gt; {obs(<math>\emptyset</math>)} </pre>
---	--	--

```

{cp( $\delta_1$ ) * obs( $\emptyset$ )}
let f = ref 41 in
let s = CreateSignal l in
CreateWaitPerm s  $\delta_1$   $\delta_0$ ;
{obs({s}) * sig(s, l, false) * waitp(s,  $\delta_0$ ) * f  $\mapsto$  41}
 $\exists b, n. \text{sig}(s, l, b) * f \mapsto n * n \neq 42 \rightarrow b = \text{false}$ 

```

<pre> {obs(<math>\emptyset</math>) * waitp(s, <math>\delta_0</math>)} (<math>\mu</math> loop () .   if &lt;if !f = 42 then () else wait s <math>\delta_0</math>;     !f = 42&gt; then ()   else     {obs(<math>\emptyset</math>) * waitp(s, <math>\delta_0</math>) * cp(<math>\delta_0</math>)}     burn <math>\delta_0</math> in loop () ) () {obs(<math>\emptyset</math>)} </pre>	<div style="border-left: 3px double black; height: 100%;"></div>	<pre> {obs({s})} &lt;f := 42; SetSignal s&gt; {obs(<math>\emptyset</math>)} </pre>
---	--	--

# Busy-waiting

```
let f = ref 41 in  
  
(μ loop ().  
  if !f = 42 then ()  
  else  
    loop ()) () || f := 42
```

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# **“Classic” spec for terminating spinlock**

# “Classic” spec for terminating spinlock

$\{R * 0 \leq n\}$  **create**  $\mathfrak{l} \ n \ \{lk.\exists\gamma. is\_lock(\gamma, lk, \mathfrak{l}, n, R)\}$

$is\_lock(\gamma, lk, \mathfrak{l}, n_1 + n_2, R) \iff is\_lock(\gamma, lk, \mathfrak{l}, n_1, R) * is\_lock(\gamma, lk, \mathfrak{l}, n_2, R)$

$\{is\_lock(\gamma, lk, \mathfrak{l}, 1, R) * obs(O) * \mathfrak{l} \prec O\}$  **acquire**  $lk \ \{\exists s. locked(\gamma, s) * R * obs(O \cup \{s\})\}$

$\{is\_lock(\gamma, lk, \mathfrak{l}, 0, R) * locked(\gamma, s) * obs(O)\}$  **release**  $lk \ \{obs(O \setminus \{s\})\}$

# “Classic” spec for terminating spinlock

$\{R * 0 \leq n\}$  create  $\mathfrak{l} n \{lk.\exists\gamma. is\_lock(\gamma, lk, \mathfrak{l}, n, R)\}$

$is\_lock(\gamma, lk, \mathfrak{l}, n_1 + n_2, R) \iff is\_lock(\gamma, lk, \mathfrak{l}, n_1, R) * is\_lock(\gamma, lk, \mathfrak{l}, n_2, R)$

$\{is\_lock(\gamma, lk, \mathfrak{l}, 1, R) * \mathbf{obs}(O) * \mathfrak{l} \prec O\}$  acquire  $lk \{\exists s. locked(\gamma, s) * R * \mathbf{obs}(O \cup \{s\})\}$

$\{is\_lock(\gamma, lk, \mathfrak{l}, 0, R) * locked(\gamma, s) * \mathbf{obs}(O)\}$  release  $lk \{\mathbf{obs}(O \setminus \{s\})\}$



# Locking patterns

```
acquire lk  || acquire lk  
// ...    || // ...  
release lk  || release lk
```



# Locking patterns

```
acquire lk  
// ...  
release lk
```

 || 

```
acquire lk  
// ...  
release lk
```



```
acquire lk  
//no release
```

 || 

```
// no acquire
```



# Locking patterns

```
acquire lk  
// ...  
release lk
```



```
acquire lk  
// ...  
release lk
```



```
acquire lk  
//no release
```



```
// no acquire
```



```
// acquire/release in different threads  
let x = ref false in
```

```
acquire lk  
x := true
```



```
// busy wait for x  
release lk
```



# Locking patterns

```
acquire lk  
// ...  
release lk
```

```
acquire lk  
// ...  
release lk
```



```
acquire  
//  
release
```



**Module controls termination reasoning**

```
// acquire lock for threads
```

```
// busy wait for x  
release lk
```



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# Atomic triple for a lock

$\langle b. \text{lock\_state}(lk, b) \rangle \text{ acquire } lk \langle \text{lock\_state}(lk, \text{true}) * b = \text{false} \rangle$

# Proving atomic triples

$$\langle b. \text{lock\_state}(lk, b) \rangle \text{acquire } lk \langle \text{lock\_state}(lk, true) * b = false \rangle \triangleq$$
$$\forall \Phi. \langle b. \text{lock\_state}(lk, b) \mid \text{lock\_state}(lk, true) * b = false \Rightarrow \Phi \rangle \multimap \text{wp acquire } lk \{ \Phi \}$$

# Proving atomic triples

$\langle b. lock\_state(lk, b) \rangle \text{ acquire } lk \langle lock\_state(lk, true) * b = false \rangle \triangleq$   
 $\forall \Phi. \langle b. lock\_state(lk, b) \mid lock\_state(lk, true) * b = false \Rightarrow \Phi \rangle \multimap \text{wp acquire } lk \{ \Phi \}$

// ...

CAS(lk, false, true)

// ...



# Proving atomic triples

$\langle b. lock\_state(lk, b) \rangle \text{ acquire } lk \langle lock\_state(lk, true) * b = false \rangle \triangleq$   
 $\forall \Phi. \langle b. lock\_state(lk, b) \mid lock\_state(lk, true) * b = false \Rightarrow \Phi \rangle \multimap \text{wp acquire } lk \{ \Phi \}$

// ...

$\{ \exists b. lock\_state(lk, b) \}$

`CAS(lk, false, true)`

// ...

# Proving atomic triples

$\langle b. lock\_state(lk, b) \rangle \text{ acquire } lk \langle lock\_state(lk, true) * b = false \rangle \triangleq$   
 $\forall \Phi. \langle b. lock\_state(lk, b) \mid lock\_state(lk, true) * b = false \Rightarrow \Phi \rangle \multimap \text{wp acquire } lk \{ \Phi \}$

// ...

$\{ \exists b. lock\_state(lk, b) \}$

`CAS(lk, false, true)`

$\left\{ \begin{array}{l} \text{if CAS unsuccessful} \quad \langle b. lock\_state(lk, b) \mid lock\_state(lk, true) * b = false \Rightarrow \Phi \rangle \\ \text{if CAS successful} \quad \Phi \end{array} \right\}$

// ...

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# Total correctness atomic triple for (unfair) lock

$\langle b$        $.lock\_state(lk, b) \rangle$  acquire  $lk$   $\langle lock\_state(lk, true) * b = false \rangle$

# Total correctness atomic triple for (unfair) lock

$\langle b \rightarrow \{false\} . lock\_state(lk, b) \rangle \text{ acquire } lk \langle lock\_state(lk, true) * b = false \rangle^{\downarrow}$

# Total correctness logically atomic triples

$$\langle \vec{x} . P \rangle e \langle \vec{v} . Q \rangle_{\varepsilon} \triangleq$$

$$\forall \Phi . \langle \vec{x} . P \mid \vec{v} . Q \Rightarrow \Phi \rangle_{\top \setminus \varepsilon} \text{wp } e \quad \{\Phi\}$$

# Total correctness logically atomic triples

$$\langle \vec{x} . P \rangle e \langle \vec{v} . Q \rangle_\varepsilon \triangleq$$

$$\forall \Phi . \langle \vec{x} . P \mid \vec{v} . Q \Rightarrow \Phi \rangle_{\top \setminus \varepsilon} \dashv^* \text{wp } e \quad \{ \Phi \}$$

$$\langle \vec{x} . P \mid \vec{v} . Q \Rightarrow \Phi \rangle_\varepsilon \vdash_\varepsilon \Vdash_\emptyset \exists \vec{x} . P *$$

( (

$$P \dashv^* \Vdash_\varepsilon \langle \vec{x} . P \mid \vec{v} . Q \Rightarrow \Phi \rangle_\varepsilon ) \wedge$$

$$( \forall \vec{v} . Q \dashv^* \Vdash_\varepsilon \Phi ) )$$

# Total correctness logically atomic triples

$$\langle \vec{x} \rightarrow X.P \rangle e \langle \vec{v}.Q \rangle_{\varepsilon}^{\downarrow} \triangleq$$

$$\forall \Phi \quad \langle \vec{x} \rightarrow X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\top \setminus \varepsilon}^{\downarrow} \dashv \ast \text{wp } e \quad \{\Phi\}$$

$$\langle \vec{x} \rightarrow X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{\downarrow} \vdash_{\varepsilon} \Vdash_{\emptyset} \exists \vec{x}. P \ast$$

( (

$$P \dashv \ast_{\varepsilon} \langle \vec{x} \rightarrow X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{\downarrow} ) \wedge$$

( (  $\forall \vec{v}. Q \dashv \ast_{\varepsilon} \Phi$  ) )



# Total correctness logically atomic triples

$$\langle \vec{x} \rightarrow X.P \rangle e \langle \vec{v}.Q \rangle_{\varepsilon}^{\downarrow} \triangleq$$

$$\forall \Phi, \beta. \quad \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\top \setminus \varepsilon}^{\downarrow} \dashv \ast \text{wp } e \quad \beta \{ \Phi \}$$

$$\langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{\downarrow} \vdash_{\varepsilon} \Downarrow_{\emptyset} \exists \vec{x}. P \ast$$

(

$$\text{wp}_{\emptyset}^{\downarrow} \beta \left\{ \text{cp}(\delta_e) \ast \quad (P \Downarrow_{\emptyset} \ast_{\varepsilon} \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{\downarrow} ) \right\} \wedge$$

$$\left( \quad \forall \vec{v}. Q \Downarrow_{\emptyset} \ast_{\varepsilon} \Phi \quad \right)$$

# Total correctness logically atomic triples

$$\langle \vec{x} \rightarrow X.P \rangle e \langle \vec{v}.Q \rangle_{\varepsilon}^l \triangleq$$

$$\forall \Phi, O, \beta. \text{obs}(O) \multimap \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\top \setminus \varepsilon}^{l;O} \multimap \text{wp } e \ \beta \{ \Phi \}$$

$$\langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l;O} \vdash_{\varepsilon} \Vdash_{\emptyset} \exists \vec{x}. P * [\top] \prec O *$$

$$\left( (\forall O. \top \prec O * \text{obs}(O) \multimap$$

$$\text{wp}_{\emptyset}^{\downarrow} \beta \left\{ \text{cp}(\delta_e) * \text{obs}(O) * (P \Vdash_{\emptyset} *_{\varepsilon} \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l;O}) \right\} \right) \wedge$$

$$\left( \forall \vec{v}. Q \Vdash_{\emptyset} *_{\varepsilon} \Phi \right)$$

# Total correctness logically atomic triples

$$\langle \vec{x} \rightarrow X.P \rangle_e \langle \vec{v}.Q \rangle_{\varepsilon}^l \triangleq$$

$$\forall \Phi, O, \beta. \text{obs}(O) \multimap \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\top \setminus \varepsilon}^{l;O} \multimap \text{wp } e \quad \beta \{ \Phi \}$$

$$\langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l;O} \vdash_{\varepsilon} \Vdash_{\emptyset} \exists \vec{x}. P * [\top] \prec O *$$

$$\left( (\forall O. \top \prec O * \vec{x} \notin X * \text{obs}(O) \multimap$$

$$\text{wp}_{\emptyset}^{\downarrow} \beta \left\{ \text{cp}(\delta_e) * \text{obs}(O) * (P \Vdash_{\emptyset} *_{\varepsilon} \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l;O}) \right\} \right) \wedge$$

$$\left( \forall \vec{v}. Q \Vdash_{\emptyset} *_{\varepsilon} \Phi \right)$$

# Total correctness logically atomic triples

$\langle b \rightarrow \{false\} . lock\_state(lk, b) \rangle$  acquire  $lk$   $\langle lock\_state(lk, true) * b = false \rangle^l$

$\langle \vec{x} \rightarrow X.P \rangle e \langle \vec{v}.Q \rangle_{\varepsilon}^l \triangleq$

$\forall \Phi, O, \beta. obs(O) \rightarrow * \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\top \setminus \varepsilon}^{l; O} \rightarrow * wp\ e \ \beta \{ \Phi \}$

$\langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l; O} \vdash_{\varepsilon} \Downarrow_{\emptyset} \exists \vec{x}. P * [\uparrow] \sim O \downarrow$

$\langle \text{let } b = \text{CAS}(lk, false, true) \text{ in } (\text{if } b \text{ then } () \text{ else } \beta); b \rangle$

$\left( (\forall O. l \prec O * \vec{x} \notin X * obs(O) \rightarrow * \right.$

$\left. wp_{\emptyset}^{\downarrow} \beta \left\{ cp(\delta_e) * obs(O) * (P \Downarrow_{\emptyset} *_{\varepsilon} \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l; O}) \right\} \right) \wedge$

$\left( \forall \vec{v}. Q \Downarrow_{\emptyset} *_{\varepsilon} \Phi \right)$

# Total correctness logically atomic triples

$$\langle \vec{x} \rightarrow X.P \rangle_e \langle \vec{v}.Q \rangle_{\varepsilon}^l \triangleq$$

$$\forall \Phi, O, \beta. \text{obs}(O) \multimap \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\top \setminus \varepsilon}^{l;O} \multimap \text{wp } e \ \beta \{ \Phi \}$$

$$\langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l;O} \vdash_{\varepsilon} \Vdash_{\emptyset} \exists \vec{x}. P * [\top] \prec O *$$

$$\left( (\forall O. \top \prec O * \vec{x} \notin X * \text{obs}(O) \multimap$$

$$\text{wp}_{\emptyset}^{\downarrow} \beta \left\{ \text{cp}(\delta_e) * \text{obs}(O) * (P \Vdash_{\emptyset} *_{\varepsilon} \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l;O}) \right\} \right) \wedge$$

$$\left( \forall \vec{v}. Q \Vdash_{\emptyset} *_{\varepsilon} \Phi \right)$$

# Total correctness logically atomic triples

$$\langle \vec{x} \rightarrow X.P \rangle e \langle \vec{v}.Q \rangle_{\varepsilon}^l \triangleq$$

$$\forall \Phi, O, \alpha, \beta. \text{obs}(O) \multimap \langle \vec{x} \xrightarrow[\alpha]{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\top \setminus \varepsilon}^{l; O} \multimap \text{wp } e \alpha \beta \{ \Phi \}$$

$$\langle \vec{x} \xrightarrow[\alpha]{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l; O} \vdash_{\varepsilon} \Vdash_{\emptyset} \exists \vec{x}. P * [\top] \prec O *$$

$$\left( (\forall O. \top \prec O * \vec{x} \notin X * \text{obs}(O) \multimap$$

$$\text{wp}_{\emptyset}^{\downarrow} \beta \left\{ \text{cp}(\delta_e) * \text{obs}(O) * (P \Vdash_{\emptyset} *_{\varepsilon} \langle \vec{x} \xrightarrow[\alpha]{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l; O}) \right\} \right) \wedge$$

$$\left( \text{obs}(O) \multimap \text{wp}_{\emptyset}^{\downarrow} \alpha \{ \forall \vec{v}. Q \Vdash_{\emptyset} *_{\varepsilon} \Phi \} \right)$$

# Total correctness logically atomic triples

$\langle b \rightarrow \{false\} . lock\_state(lk, b) \rangle$  acquire  $lk$   $\langle lock\_state(lk, true) * b = false \rangle^l$

$\langle \vec{x} \rightarrow X.P \rangle e \langle \vec{v}.Q \rangle_{\varepsilon}^l \triangleq$

$\forall \Phi, O, \alpha, \beta. obs(O) \rightarrow * \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\top \setminus \varepsilon}^{l; O} \rightarrow * wp\ e\ \alpha\ \beta\ \{\Phi\}$

$\langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l; O} \vdash_{\varepsilon} \exists \vec{x}. P * [l]_{\varepsilon} \wedge O *$

`<let b = CAS(lk, false, true) in  
(if b then  $\alpha$  else  $\beta$ ); b>`

$\left( (\forall O. l \prec O * \vec{x} \notin X * obs(O) \rightarrow * \right.$

$\left. wp_{\emptyset}^{\downarrow} \beta \left\{ cp(\delta_e) * obs(O) * (P \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi)_{\varepsilon}^{l; O} \right\} \right) \wedge$

$\left( obs(O) \rightarrow * wp_{\emptyset}^{\downarrow} \alpha \left\{ \forall \vec{v}. Q \xrightarrow{\beta} \Phi \right\} \right)$



# Total correctness logically atomic triples

$$\langle \vec{x} \rightarrow X.P \rangle e \langle \vec{v}.Q \rangle_{\varepsilon}^l \triangleq$$

$$\forall \Phi, O, \alpha, \beta. \text{obs}(O) \multimap \langle \vec{x} \xrightarrow[\alpha]{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\top \setminus \varepsilon}^{l;O} \multimap \text{wp } e \alpha \beta \{ \Phi \}$$

$$\langle \vec{x} \xrightarrow[\alpha]{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l;O} \vdash_{\varepsilon} \exists \vec{x}. P * [\top] \prec O *$$

$$\left( (\forall O. \top \prec O * \vec{x} \notin X * \text{obs}(O) \multimap$$

$$\text{wp}_{\emptyset}^{\downarrow} \beta \left\{ \text{cp}(\delta_e) * \text{obs}(O) * (P \multimap_{\varepsilon} \langle \vec{x} \xrightarrow[\alpha]{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\varepsilon}^{l;O}) \right\} \right) \wedge$$

$$\left( \text{obs}(O) \multimap \text{wp}_{\emptyset}^{\downarrow} \alpha \{ \forall \vec{v}. Q \multimap_{\varepsilon} \Phi \} \right)$$



1. Verifying deadlock-freedom
2. Verifying absence of infinite recursion
3. Verifying termination of busy-waiting programs
4. Modular specifications
  - A. Logically atomic triples
  - B. Total correctness logically atomic triples with liveness assumption

## **5. Conclusion**

# Conclusion

We propose:

- Modular specifications for total correctness of busy-waiting concurrent modules

We currently have:

- VeriFast proofs of spinlocks and ticketlocks
- Some building blocks in Coq/Iris
- The belief that the approach scales to cohort locks

# Some references

**In addition to Iris, this work is heavily influenced by**

- D’Oswaldo, Emanuele, Julian Sutherland, Azadeh Farzan, and Philippa Gardner. “TaDA Live: Compositional Reasoning for Termination of Fine-Grained Concurrent Programs.” *ACM Transactions on Programming Languages and Systems* 43, no. 4 (December 31, 2021): 1–134. <https://doi.org/10.1145/3477082>.
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**In addition to Iris, this work is heavily influenced by**

- Jacobs, Bart, Dragan Bosnacki, and Ruurd Kuiper. “Modular Termination Verification of Single-Threaded and Multithreaded Programs.” *ACM Transactions on Programming Languages and Systems* 40, no. 3 (September 30, 2018): 1–59. <https://doi.org/10.1145/3210258>.
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# Backup slides

# **wp**<sup>↓</sup>

## **Definition**

$$\text{wp}_{\mathcal{E}}^{\downarrow} e \{v. P\} \triangleq \forall \sigma, n_s, \vec{k}, n_t. S(\sigma, n_s, \vec{k}, n_t) \equiv *_{\mathcal{E}} \\ \exists \sigma', v. (e, \sigma \downarrow v, \sigma') * S(\sigma', n_s, \vec{k}, n_t) * P(v)$$

# $\text{wp}^{\Downarrow}$ Lemmas

BIG-STEP-ATOMIC

$$\varepsilon_1 \Rightarrow \varepsilon_2 \text{wp}_{\varepsilon_2}^{\Downarrow} e \{v. \varepsilon_2 \Rightarrow \varepsilon_1 P\} \vdash \text{wp}_{\varepsilon_1}^{\Downarrow} e \{v. P\}$$

BIG-STEP-BIND

$K$  is a context

---

$$\text{wp}_{\varepsilon}^{\Downarrow} e \{v. \text{wp}_{\varepsilon}^{\Downarrow} K[v] \{w. P\}\} \vdash \text{wp}_{\varepsilon}^{\Downarrow} K[e] \{w. P\}$$

BIG-STEP-ATOMICBLOCK

$$\text{wp}_{\varepsilon}^{\Downarrow} e \{v. P\} \vdash \text{wp}_{\varepsilon} \langle e \rangle \{v. P\}$$



# HeapLang<sup><</sup>

## Head step rules

- Convention: \ has precedence over  $\uplus$
- $\theta$  : thread id
- $\tau$  : “thread phase”, to prevent self-fueling busy-waiting
- AtomicBlock uses big-step evaluation relation that matches the operational semantics but precludes forking

$$\begin{array}{c} \text{BURNS} \\ \tau' = \sigma.\text{PHASE}(\theta) \quad \tau = \max_{\sqsubseteq} \{ \tau \mid (\tau, \delta) \in \sigma.\text{CALLPERMS} \wedge \tau \sqsubseteq \tau' \} \\ \frac{(\tau, \delta) \in \sigma.\text{CALLPERMS} \quad \delta' < \delta \quad 0 \leq n}{\text{Burn}(e, \delta, n, \delta'), \sigma \xrightarrow[\theta]{\epsilon} e, \sigma : \text{CALLPERMS} \setminus \{(\tau, \delta)\} \uplus (n \cdot (\tau, \delta'))} \end{array}$$

$$\begin{array}{c} \text{CREATE SIGNALS} \\ s \notin \sigma.\text{SIGNALS} \quad \mathfrak{l} \in \mathcal{L} \\ \frac{}{\text{CreateSignal}(\mathfrak{l}), \sigma \xrightarrow[\theta]{\epsilon} (), \sigma : \text{SIGNALS}[s \leftarrow (\mathfrak{l}, \text{false})] : \text{OBLIGATIONS}(\theta) \cup \{s\}} \end{array}$$

$$\begin{array}{c} \text{SET SIGNALS} \\ \sigma.\text{SIGNALS}(s) = (\mathfrak{l}, -) \\ \frac{}{\text{SetSignal}(s), \sigma \xrightarrow[\theta]{\epsilon} (), \sigma : \text{SIGNALS}[s \leftarrow (\mathfrak{l}, \text{true})] : \text{OBLIGATIONS}(\theta) \setminus \{s\}} \end{array}$$

$$\begin{array}{c} \text{CREATE WAIT PERMS} \\ \tau' = \sigma.\text{PHASE}(\theta) \quad \tau = \max_{\sqsubseteq} \{ \tau \mid (\tau, \delta) \in \sigma.\text{CALLPERMS} \wedge \tau \sqsubseteq \tau' \} \\ \frac{(\tau, \delta) \in \sigma.\text{CALLPERMS} \quad \delta' < \delta}{\text{CreateWaitPerm}(s, \delta, \delta') \xrightarrow[\theta]{\epsilon} (), \sigma : \text{CALLPERMS} \setminus \{(\tau, \delta)\} : \text{WAITPERMS} \cup (s, (\tau, \delta'))} \end{array}$$

$$\begin{array}{c} \text{WAITS} \\ \tau' = \sigma.\text{PHASE}(\theta) \quad \tau = \min_{\sqsubseteq} \{ \tau \mid (s, (\tau, \delta)) \in \sigma.\text{WAITPERMS} \wedge \tau \sqsubseteq \tau' \} \\ \sigma.\text{SIGNALS}(s) = (\mathfrak{l}, \text{false}) \\ \mathfrak{l} \prec \sigma.\text{OBLIGATIONS}(\theta) \quad (s, (\tau, \delta)) \in \sigma.\text{WAITPERMS} \\ \frac{}{\text{Wait}(s, \delta) \xrightarrow[\theta]{\epsilon} (), \sigma : \text{CALLPERMISSIONS} \uplus (\tau', \delta)} \end{array}$$

$$\begin{array}{c} \text{ASSERT NO OBS} \\ \sigma.\text{OBLIGATIONS}(\theta) = \emptyset \\ \frac{}{\text{AssertNoObs}, \sigma \xrightarrow[\theta]{\epsilon} (), \sigma} \end{array}$$

$$\begin{array}{c} \text{FORKS} \\ \tau = \sigma.\text{PHASE}(\theta) \\ \text{sig}s \text{ set of signals} \quad \theta' = \min(\text{ThreadId} \setminus \text{dom}(\sigma.\text{OBLIGATIONS})) \\ \frac{}{\text{fork}(e, \text{sig}s), \sigma \xrightarrow[\theta]{\epsilon} (), \sigma : \text{OBLIGATIONS}(\theta) \setminus \text{sig}s : \text{OBLIGATIONS}(\theta') \cup \text{sig}s \\ : \text{PHASE}[\theta \leftarrow \tau.\text{Forker}; \theta' \leftarrow \tau.\text{Forkee}], (e; \text{AssertNoObs})} \end{array}$$

$$\begin{array}{c} \text{ATOMIC BLOCKS} \\ e, \sigma \Downarrow v, \sigma' \\ \frac{}{\text{AtomicBlock}(e), \sigma \xrightarrow[\theta]{\epsilon} v, \sigma'} \end{array}$$

# Total correctness logically atomic triples

## With liveness assumption

We reflect “rounds” of waiting with r/R. An example is the current owner of a ticketlock changing. This is our approach to enable waiting based on a module’s internal termination argument (e.g. the ticket-based queue).

$$\langle \vec{x} \xrightarrow{r} X.P \rangle e \langle \vec{v}.Q \rangle_{\mathcal{E}}^l \triangleq$$

$$\forall \Phi, \tau, O, R, \alpha, \beta. \text{obs}(\tau, O) \multimap \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\tau \setminus \mathcal{E}}^{l;O} \multimap \text{wp } e \alpha \beta \{ \Phi \}$$

$$\langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\mathcal{E}}^{l;O} \vdash_{\mathcal{E}} \text{wp}_{\emptyset} \exists \vec{x}. P * \llbracket l \rrbracket \prec O *$$

$$\left( (\forall O'. l \prec O' * (\exists r_0. R(r_0) * (r_0 = r \vee \text{cp}(\tau', \delta'_e) * \vec{x} \notin X * \text{obs}(\tau, O'))) \multimap$$

$$\text{wp}_{\emptyset}^{\downarrow} \beta \left\{ \text{cp}(\tau, \delta_e) * R(r) * \text{obs}(\tau, O') * (P \text{wp}_{\emptyset} *_{\mathcal{E}} \langle \vec{x} \xrightarrow{\beta} X.P \mid \vec{v}.Q \Rightarrow \Phi \rangle_{\mathcal{E}}^{l;O}) \right\} \wedge$$

$$\left( R(-) * \text{obs}(\tau, O) \multimap \text{wp}_{\emptyset}^{\downarrow} \alpha \{ \forall \vec{v}. Q \text{wp}_{\emptyset} *_{\mathcal{E}} \Phi \} \right)$$

# “Tricky client”

## Unfair spinlock is terminating in the right context

```
let x = ref false in
```

```
acquire lk | // busy wait for x | x := true  
x := true | (μ loop (). |  
release lk |   acquire(lk); |  
            | let d = !x in |  
            | release(lk); |  
            | if d then () |  
            | else loop ()) () |
```

Terminating for **fair** locks under fair scheduling

Terminating for **fair and unfair** locks under fair scheduling